

Abstract of the Disclosure:

A device for the bidirectional transfer of data between two processors contains input and output control information memories for storing an item of binary control information for 5 input and output memories. The memories can be accessed by the second processor and, via a DMA channel, by the first processor. The access operations of both processors to these memories are coordinated on the basis of the content of the input and output control information memories.

RELEASER: [unclear]
DATE: [unclear]

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